

Article

Fast Coordinated Control of DFIG Wind Turbine Generators for Low and High Voltage Ride-Through

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Abstract: This paper presents a fast coordinated control scheme of the rotor side converter (RSC), the Direct Current (DC) chopper and the grid side converter (GSC) of doubly fed induction generator (DFIG) wind turbine generators (WTGs) to improve the low voltage ride through (LVRT) and high voltage ride through (HVRT) capability of the DFIG WTGs. The characteristics of DFIG WTGs under voltage sags and swells were studied focusing on the DFIG WTG stator flux and rotor voltages during the transient periods of grid voltage changes. The protection schemes of the rotor crowbar circuit and the DC chopper circuit were proposed considering the characteristics of the DFIG WTGs during voltage changes. The fast coordinated control of RSC and GSC were developed based on the characteristic analysis in order to realize efficient LVRT and HVRT of the DFIG WTGs. The proposed fast coordinated control schemes were verified by time domain simulations using Matlab-Simulink.

Keywords: coordinated control; doubly-fed induction generator (DFIG); grid side converter (GSC); high voltage ride through (HVRT); low voltage ride through (LVRT); rotor side converter (RSC)

1. Introduction

The doubly fed induction generator (DFIG) is one of the most popular types of generators used in large scale wind turbines with several advantages compared to other wind turbine generators (WTGs) [1]. However, because the stator windings are directly connected to the grid, DFIG WTGs are quite sensitive to grid disturbances. In the past, WTGs were allowed to disconnect from the grid during low voltage conditions. However, with the rapidly increased penetration of wind power and concentrated wind power installation, disconnection of WTGs due to low voltage conditions can cause serious problems on the power system stability. After the low voltage fault is cleared, high voltage conditions may be induced if there is no fast automatic control of the reactive power, and more wind turbines will be tripped due to the high voltage conditions. Many grid codes require wind power plants (WPPs) to remain connected to the grid during low or high voltage conditions, referred to as low voltage ride through (LVRT) or high voltage ride through (HVRT) requirements [2,3].

In recent studies, the characteristics of voltage sags at the point of common coupling (PCC) of WPPs are studied with consideration of grid-angle jumps [4–7]. In [6,7], the effects of phase-angle jump on transient DFIG operation are discussed, and it is shown that large phase-angle jumps have significant impacts on DFIG transient behaviour. These studies are based on the voltage divider model with different X/R ratios of impedance and the detailed discussion is focused on the amplitude of the fault voltage and grid-fault angle. However, the overvoltage conditions during the voltage recovery period have not been investigated.

The transient behaviour of DFIG WTGs under grid fault conditions has been comprehensively investigated by analysing the transient electromagnetic force characteristic of stator flux [8–12], and the oscillating response of the DC link voltage was studied in [13,14]. Many solutions have been proposed to solve the LVRT problem for DFIGs, which can be categorized into two types including “the crowbar protection method” and “the demagnetizing method”. An additional hardware crowbar circuit with resistors can protect the rotor side converter (RSC) from rotor overcurrent [15–17]. A similar circuit called ‘DC-chopper’ is used to protect the DC link from overvoltage [18,19]. However, the existing methods have not considered the voltage swell faults under phase-angle jump conditions. In addition to the hardware solutions, many control strategies have been developed for improving the transient response performance of DFIG WTGs under grid voltage sags [20–22]. However, most methods have not considered that the converter capacity limitations can influence the DC-overvoltage especially for HVRT and are difficult to implement without considering the phase-angle jump.

This paper aims to further analyse the transient response of DFIG WTGs with consideration of phase-angle jumps under both grid voltage sags and swells. A rotor crowbar protection scheme used for voltages sag or swell is developed, and a new DC chopper protection scheme coordinated with the active power of the RSC and grid side converter (GSC) is proposed, which can prevent DC links from

overvoltage and voltage oscillation. A voltage assessment method considering phase-angle jumps is used in this paper, and the amplitude of the fault voltage and the phase angle for vector control of converters can be obtained correctly. The transient stator flux compensation component is used as feed-forward factor for the vector control, which can improve the transient performance of the RSC controller. In order to keep the DC voltage under the maximum threshold during the grid voltage swell, a reactive current control scheme of the GSC is proposed in this paper. Case studies using Matlab-Simulink (The MathWorks, Natick, MA, USA) have been conducted in order to verify the effectiveness of the proposed protection schemes and control strategies.

The paper is organized as follows: the analysis of the grid fault conditions is described in Section 2. The behavior of the DFIG WTG and GSC under voltage sag and swell conditions is discussed in Section 3. The protection schemes of DFIGs for fault ride through (FRT) are explained in Section 4 and the enhanced GSC and RSC control of DFIGs is presented in Section 5. The case studies are described and discussed in Section 6. Finally, the conclusions are drawn.

2. Analysis of Grid Fault Conditions

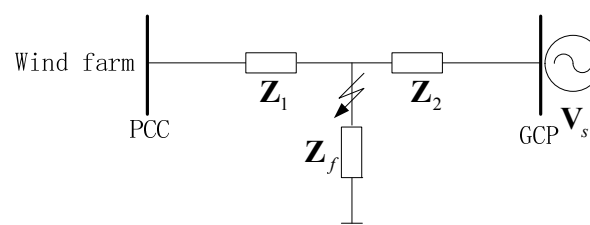
This section analyzes both grid voltage sag and swell fault conditions at the point of common coupling (PCC) of a wind farm. The voltage-divider model is widely used in the analysis of characteristics of grid faults in radial systems [4–7] and is used for the grid voltage sag and swell fault conditions at the PCC of a wind farm.

Figure 1 shows the voltage-divider model with the impedance between the wind farm and the fault location where V_s is the pre-fault voltage, Z_1 is the impedance between the wind farm and the fault location, Z_2 is the impedance between the grid connection point (GCP) and the fault location, and Z_f is the impedance between the fault location and ground. In the voltage-divider model, the wind farm current before as well as during the fault is neglected [4]. The voltage at the PCC during grid faults can then be defined as:

$$V_{\text{sag}} = \frac{Z_f}{Z_f + Z_2} V_s \quad (1)$$

where V_{sag} is the voltage at the PCC during the fault.

Figure 1. Voltage-divider model.



The sum of Z_1 and Z_2 is the equivalent impedance of the external grid and can be defined as Z_s which can be expressed as:

$$Z_s = \frac{|V_s|^2}{k \cdot S} e^{j\alpha_s} \quad (2)$$

where α_s is the impedance angle of Z_s , and S is the installed capacity of the wind farm, and k is the ratio of the system strength to the wind farm installed capacity. Define p as the ratio of the distance between the fault location and the PCC to the distance between the PCC and the GCP, and consider Z_s is linear by step on the transmission line, Z_2 can be expressed as:

$$Z_2 = pZ_s = \frac{p|V_s|^2}{k \cdot S} e^{j\alpha_s} \quad (3)$$

Similarly, by defining q as the ratio of Z_f to Z_s , which depends on the fault location, Z_s and Z_f have the following relation:

$$\frac{Z_f}{Z_2} = \frac{q}{p} e^{j(\alpha_f - \alpha_s)} = \lambda e^{j\alpha} \quad (4)$$

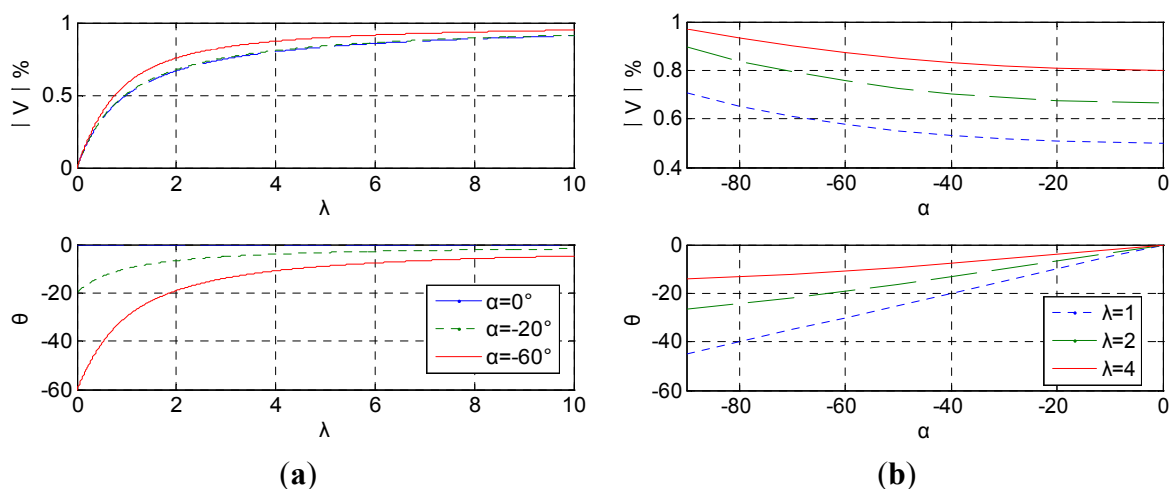
where α_f is the impedance angle of Z_2 , λ represents the relative fault distance, and α is fixed for any given source [4], such as 0° (the most common value for transmission systems), -20° (typical value for distribution systems), or -60° (the minimum value for wind farms located offshore with submarine alternating current (AC) cables). Using Equations (1) and (4), the voltage at the PCC during grid faults can be obtained as follows:

$$V_{\text{sag}} = \frac{\lambda e^{j\alpha}}{1 + \lambda e^{j\alpha}} V_s \quad (5)$$

2.1. Symmetrical Voltage Sag Fault

It is shown from Equation (2) to Equation (5) that V_{sag} depends on λ and α . For voltage sags under symmetrical fault conditions (Type A), Figure 2 shows the voltage magnitude and phase-angle jump with respect to the change of λ or α .

Figure 2. Magnitude and phase-angle jump under grid fault Type A. (a) α is fixed; (b) λ is fixed.



It is shown in Figure 2a, with the same impedance angle α , both fault depth (1-magnitude of fault voltage) and phase-angle jump decreases as λ increases; it is shown in Figure 2b, with the same relative fault distance λ , phase-angle jump decreases as $|\alpha|$ decreases (when $\alpha = 0^\circ$, no phase-angle jump), however, decreasing $|\alpha|$ will increase fault depth.

2.2. Voltage Swell Condition

There are many situations causing voltage swells in the grid, such as load shedding or unbalanced grid faults. According to the analysis of the data from the fault recorders, voltage swells may occur during the low voltage fault recovery period. The cascading wind turbine tripping in China shows that the large scale wind turbine tripping was initially triggered by a low voltage condition caused by an electric component phase to phase fault and quite a number of wind turbines tripped. After the fault was cleared, high voltage condition was induced because there was no automatic control of reactive power compensation within WPPs and more wind turbines tripped due to the over-voltage conditions.

The resulting over-voltage condition may have different magnitudes and durations, depending on the disturbance. Similar to the grid voltage dip analysis results, overshoot of amplitude and phase-angle jump may occur during the voltage recovery, depending on the impedance angle α , the relative fault distance λ , the type and capacity of reactive power compensation equipment.

3. Behavior of DFIG WTG and GSC under Voltage Sag and Swell

This section is to analyze the dynamic responses of the DFIG WTG and GSC under voltage sag and swell conditions, and evaluate the impact on both the RSC and the GSC.

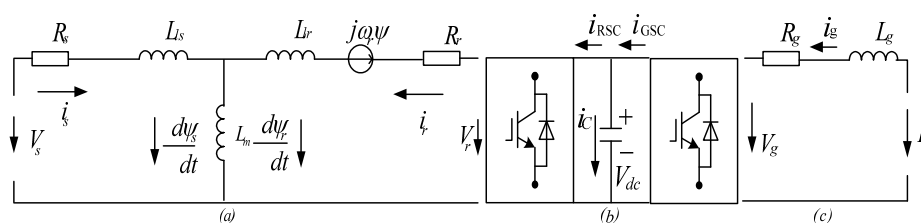
3.1. DFIG Model and GSC Model

Figure 3a shows the “T-form” equivalent circuit of a DFIG following the motor direction. The stator- and rotor-side vectors in the stator reference frame with all the parameters converted to the stator are:

$$\begin{cases} \vec{v}_s^s = R_s \vec{i}_s^s + \frac{d\vec{\psi}_s^s}{dt} \\ \vec{v}_r^s = R_r \vec{i}_r^s + \frac{d\vec{\psi}_r^s}{dt} + j\omega_r \vec{\psi}_r^s \end{cases} \quad (6)$$

where ω_r is the rotor speed, and subscripts s and r indicate stator and rotor.

Figure 3. Equivalent circuit of: (a) DFIG from the stator side; (b) DC link; and (c) External grid.



The flux linkage equations of the stator and rotor are:

$$\begin{cases} \vec{\psi}_s = L_s \vec{i}_s + L_m \vec{i}_r \\ \vec{\psi}_r = L_m \vec{i}_s + L_r \vec{i}_r \end{cases} \quad (7)$$

where L_s , L_r and L_m are the stator inductance, rotor inductance and mutual inductance, respectively.

Using Equations (6) and (7), the rotor voltage vector and the stator flux are obtained as Equations (8) and (9):

$$\vec{v}_r = \frac{L_m}{L_s} \left(\frac{d}{dt} - j\omega_r \right) \vec{\psi}_s + (R_r - \sigma L_r) \left(\frac{d}{dt} - j\omega_r \right) \vec{i}_r \quad (8)$$

$$\frac{d\vec{\psi}_s}{dt} = \vec{V}_s - \frac{R_s}{L_s} \vec{\psi}_s + \frac{R_s L_m}{L_s} \vec{i}_r \quad (9)$$

where $\sigma = 1 - L_m^2/L_s \cdot L_r$ is the linkage coefficient.

It is shown from Equation (8) that the rotor voltage can be decomposed into two parts (the electromotive force (EMF) and the voltage drop caused by rotor current) [8].

Figure 3b shows the circuit of DC link. The equation of the DC link voltage is:

$$C \frac{d\vec{v}_{dc}}{dt} = \vec{i}_{GSC} - \vec{i}_{RSC} \quad (10)$$

Figure 3c shows the grid side equivalent circuit. Neglecting the line side resistance R_g , the terminal voltage of the GSC is:

$$\vec{v}_g = \vec{E} - L_g \frac{d\vec{i}_g}{dt} - j\omega_s L_g \vec{i}_g \quad (11)$$

where L_g is the line side inductance of the GSC. In the voltage oriented reference frame, the d- and q-components of the GSC voltage are:

$$\begin{cases} v_{gd} = E_{gd} - L_g \frac{di_{gd}}{dt} + \omega_s L_g i_{gq} \\ v_{gq} = -L_g \frac{di_{gq}}{dt} - \omega_s L_g i_{gd} \end{cases} \quad (12)$$

3.2. Behavior of the DFIG WTG under Voltage Sag Condition

Considering the magnitude of the DFIG parameters (about 10^{-3}), the last part of Equation (9) can be neglected, then Equation (9) is simplified as a first-order differential equation to the stator flux, and the boundary condition is:

$$\vec{\psi}_s(t_0^-) = \vec{\psi}_s(t_0^+) \quad (13)$$

where t_0 is the moment when voltage sag or jump occurs. The solution of Equation (9) is obtained as:

$$\vec{\psi}_{s_dc} = \frac{V_{s_dc}}{j\omega_s} e^{\frac{t}{\tau}} \quad (14)$$

where $\tau = L_s/R_s$ is the time constant of the stator flux, ω_s is the stator angular frequency, and V_{s_dc} is derived by Equation (10), which is a DC component and excited only during the dynamic operation. During normal operation, neglecting the stator resistance, the stator flux can be expressed as follows:

$$\vec{\psi}_{s_ac} = \frac{V_s}{j\omega_s} e^{j\omega_s t} \quad (15)$$

In the analysis, the flux imposed by the stator voltage can be calculated by Equations (11) and (12):

$$\vec{\Psi}_s = \vec{\Psi}_{s_ac} + \vec{\Psi}_{s_dc} = (1-P) \frac{V_s}{j\omega_s} e^{j(\omega_s t + \theta)} + \frac{V_s [e^{j\omega_s t_0} - (1-P)e^{j(\omega_s t_0 + \theta)}]}{j\omega_s} e^{-\frac{t}{\tau}} \quad (16)$$

where P is the depth of the voltage sag or swell, θ is the phase-angle jumps of the voltage.

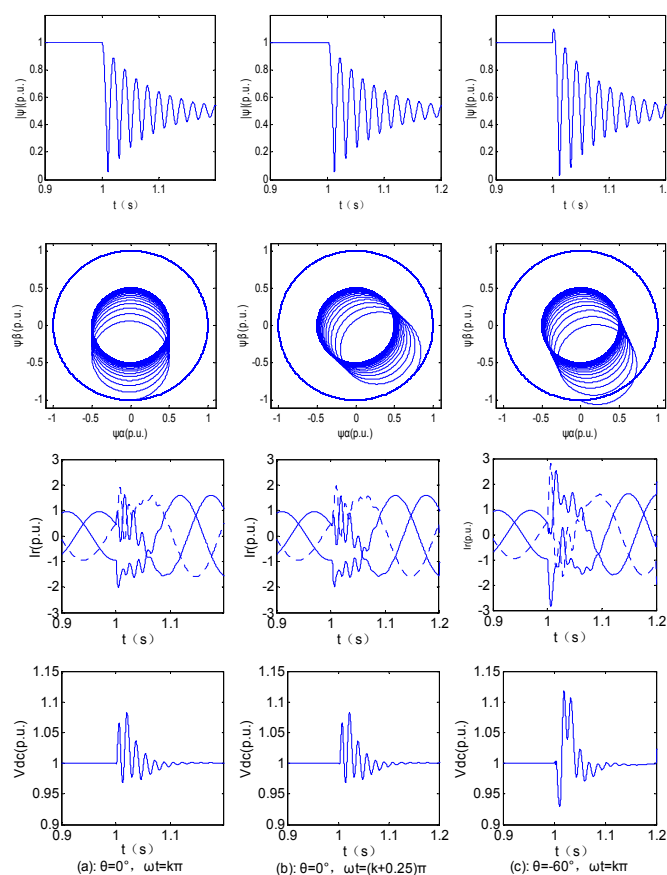
In Figure 4a,b, it is shown that the dynamic response of the DFIG and the DC link voltage is independent of the voltage phase when faults occur. With the phase-angle jump increasing, the overshoots of the rotor flux and the rotor current increases, as Figure 4c shows. By solving Equations (8) and (16), the rotor voltage in the rotor frame is:

$$\vec{v}_r^r = \vec{v}_{r0}^r + R_r \vec{i}_r^r + \sigma L_r \frac{d\vec{i}_r^r}{dt} \quad (17)$$

Where \vec{v}_{r0}^r is the EMF part:

$$\vec{v}_{r0}^r = \frac{L_m}{L_s} [s V_s (1-P) e^{j(\omega_s t + \theta)} - (\frac{1}{\tau} + (1-s)j\omega_s) \vec{\Psi}_{s_dc} e^{-j\omega_s t}] \quad (18)$$

Figure 4. Simulation results for three-phase faults (0.5 p.u.) with different voltage phase when faults occur and phase-angle jumps (a) phase angle jump $\theta = 0^\circ$, $\omega t = k\pi$; (b) phase angle jump $\theta = 0^\circ$, $\omega t = (k + 0.25)\pi$; and (c) phase angle jump $\theta = 60^\circ$, $\omega t = k\pi$.



Under a severe situation ($P = 1$, $\theta = 0$), for example, before and after the fault occurs, the aptitude of V_{r0} changes from $[L_m \cdot V_s \cdot s / L_s]$ to $[L_m \cdot (1-s) \cdot V_s / L_s]$ according to Equation (18). Assuming $s = 0.3$, the overvoltage value of V_{r0} is 2.3 times of the nominal value, and it is 4.3 times assuming $s = -0.3$. The results show that when grid faults occur, a large rotor voltage is required to control the rotor current.

Since the performance of the RSC controller is restricted by its capability, the rotor current cannot follow such a reference, which leads to over current, and then the over current protection of the RSC is activated. Since the active power output of the GSC is substantially reduced after a grid fault, a large quantity of energy from the RSC flows into the DC link which causes the DC voltage to increase and oscillate.

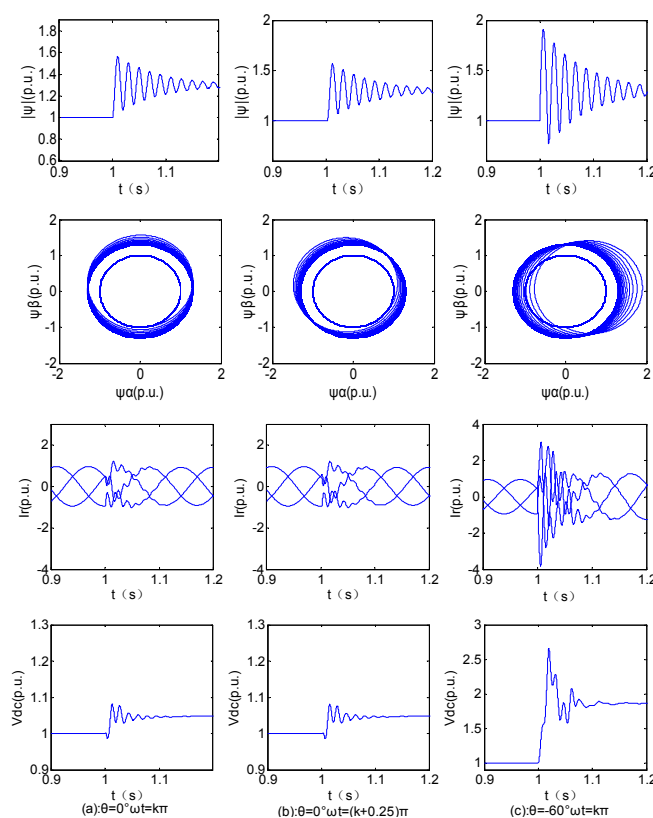
3.3. Behavior of the DFIG WTG under Voltage Swell Conditions

Figure 5 shows the simulation results considering the voltage magnitude and the phase-angle jump during the grid fault recovery. It is shown that the operating characteristics of the DFIG WTG under voltage swells are similar to those under voltage sags. However, after the transient oscillation, the DC voltage increases up to a high value because the line side voltage exceeds the maximum voltage of the GSC, which is:

$$|v_{\text{glrms,max}}| = M v_{\text{dc}} \quad (19)$$

where M is the maximum voltage utilization. The minimum value of v_{dc} is determined by M and $|v_{\text{glrms}}|$ ($M = \sqrt{2}$ for SVPWM; $= 2\sqrt{2}/\sqrt{3}$ for SPWM; and $= 1.35$ for diode rectification, which is the minimum limitation). Based on Equation (19), the minimum DC voltage value increases with the high grid voltage, which may exceed the maximum controllable DC voltage range of the GSC, and lead to continuous overvoltage of the DC link during the whole process of the high voltage condition.

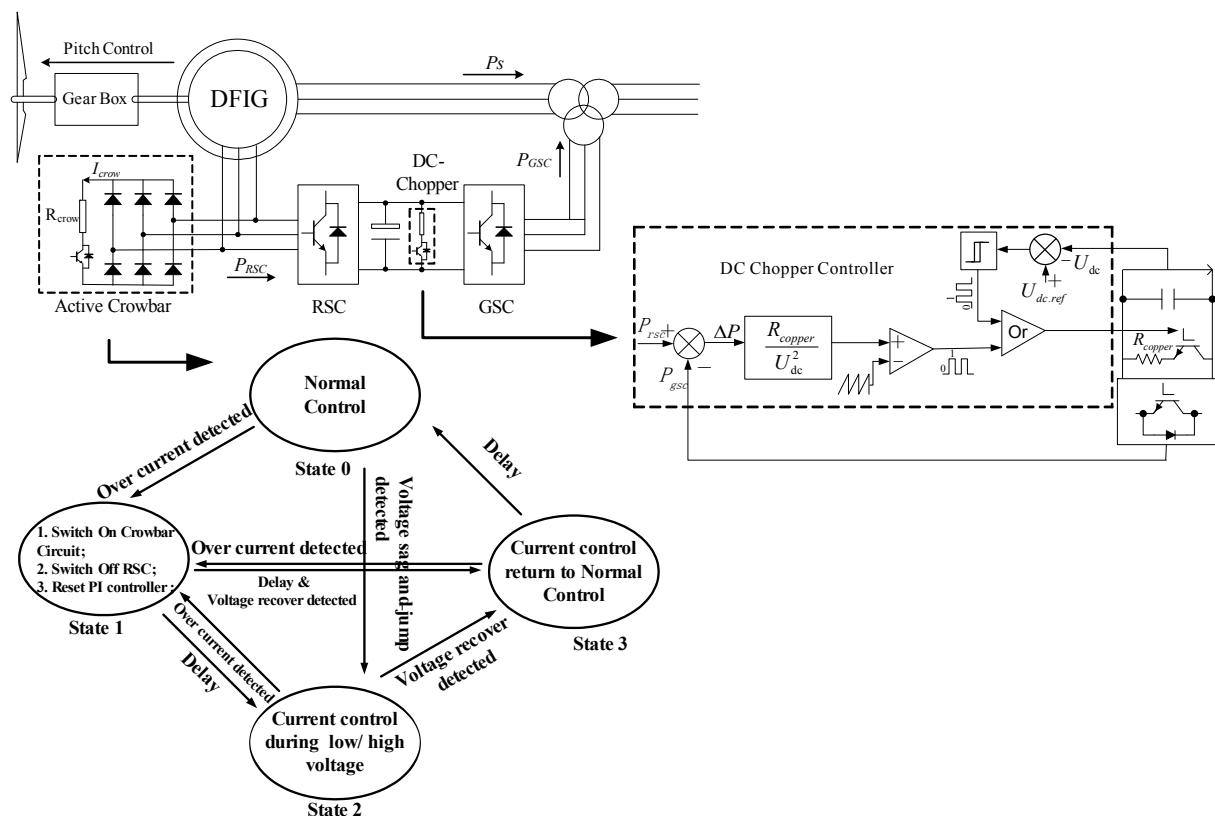
Figure 5. Simulation results for three-phase faults (1.3 p.u.) with different voltage phases when faults and phase-angle jump occur (a) phase angle jump $\theta = 0^\circ$, $\omega t = k\pi$; (b) phase angle jump $\theta = 0^\circ$, $\omega t = (k + 0.25)\pi$; and (c) phase angle jump $\theta = 60^\circ$, $\omega t = k\pi$.



4. Protection Schemes of the DFIG WTG for Fault Ride Through

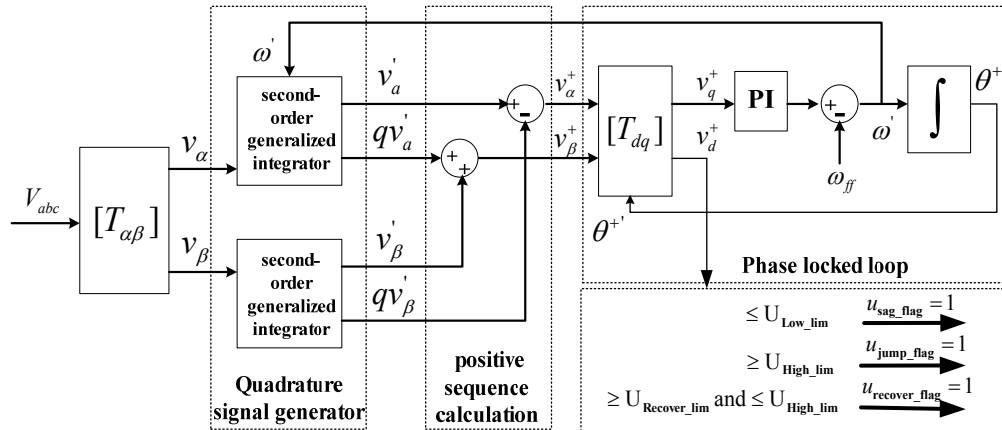
It is known from the above section that the overcurrent on rotor windings and overvoltage at the DC link occurs during the grid voltage sags or swells. In order to protect the WTG and keep it stay connected to the grid under the grid code specified low voltage or high voltage conditions, the protection schemes of the DFIG WTG with active crowbar and DC chopper circuits are described in this section. The structure of the protection schemes is illustrated in Figure 6.

Figure 6. DFIG WTG model with protection schemes.



4.1. Active Crowbar Protection Scheme

In order to realize HVRT/LVRT, a set of control logics of the crowbar circuit has been developed and is shown in Figure 6 based on the finite state machine design method. Whether the crowbar is to be activated depends on the flag of the overcurrent detection. The overcurrent threshold is usually 1.5 to 2.5 times the rated value. As shown in State 1, with the crowbar circuit “switched on” and the RSC “switched off”, the integration of the rotor current PI controller should be reset, and the reference value should be set as the feedback value, which is to avoid the saturation of the Proportional Integral (PI) controller. Considering the short grid reclosing time (0.15 s to 0.5 s) and the response of the automatic throw-in equipment for emergency power supply (0.2 s to 0.5 s), the recovery delay is set from state 3 (current control) to state 4 (normal control). For the current control of the RSC during the low or high grid voltage conditions, it is required to rapidly detect the depth of voltage sag or swell. Considering asymmetric characteristics and phase-angle jump of actual grid faults, a fast detection method based on the second-order generalized integrator is used and is shown in Figure 7.

Figure 7. Scheme of grid fault detection method.

After filtering with the second-order generalized integrator, v_α/v_β and its orthogonal signal qv_α/qv_β are obtained, where q is the signal delayed $T_s/4 (= e^{-j\pi/2})$, so the positive voltage can be calculated as:

$$\begin{aligned} v_a^+ &= \frac{1}{2}v_\alpha - \frac{q}{2}v_\beta \\ v_\beta^+ &= \frac{q}{2}v_\alpha + \frac{1}{2}v_\beta \end{aligned} \quad (20)$$

The transfer functions of the second-order generalized integrator are:

$$\begin{aligned} D(s) &= \frac{v'}{v}(s) = \frac{k\omega s}{s^2 + k\omega s + \omega^2} \\ Q(s) &= \frac{qv'}{v}(s) = \frac{k\omega^2}{s^2 + k\omega s + \omega^2} \end{aligned} \quad (21)$$

where ω is the resonance frequency and k is the damping ratio. Figure 8 shows the performance of this method simulated by Matlab-Simulink, and it can be seen when grid voltage jump to 1.3 p.u. with phase-angle jump to -60° at 0.1 s, the positive sequence voltage and phase angle can be calculated in $T_s/4$.

4.2. DC Chopper Control Scheme

A chopper circuit with a resistor is added to reduce the overvoltage and voltage-fluctuation of the DC-link during grid voltage sags and swells, and Figure 6 shows its control scheme. As seen from Equation (10), the reason of the DC-voltage fluctuation is the unbalanced current from the RSC and the GSC. Neglecting the power losses of the converters and considering the active power negligible flowing into the grid, Equation (10) can be expressed as:

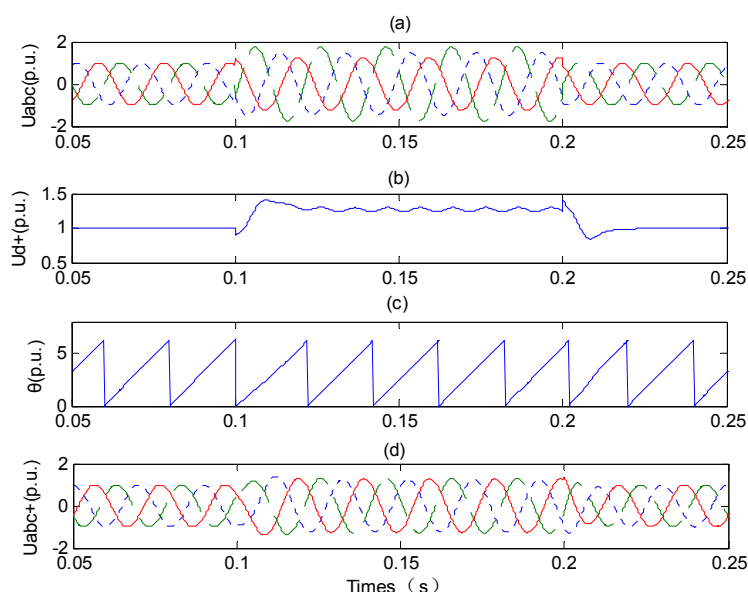
$$\frac{1}{2}C \frac{dV_{dc}^2}{dt} = P_{GSC} - P_{RSC} \quad (22)$$

Based on Equation (22), during the grid disturbance, by dissipating the extra energy into the resistor of the DC chopper, the fluctuation of the DC voltage can be reduced, so the duty of the DC-chopper switch is derived as:

$$D = \frac{R_{dc_chopper}}{V_{dc}^2} (P_{GSC} - P_{RSC}) \quad (23)$$

It is shown in Figure 6, the IGBT of the DC chopper is activated in order to balance the active power between the grid side and the rotor side based on Equation (23), and it is also triggered when the DC voltage exceeds its limitation, which is to protect the DC capacitor from over-voltage especially during HVRT.

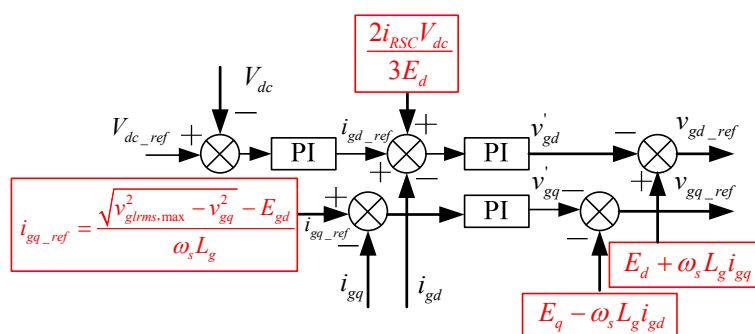
Figure 8. Simulation results for grid voltage detection method (positive sequence magnitude: 1.3 p.u.; negative sequence aptitude: 0.3 p.u.; positive sequence phase-angel jump: -60° ; negative sequence phase-angel jump: 0°). (a) three phase grid voltages; (b) the positive amplitude value of grid voltage; (c) the angel of grid voltage; and (d) the positive components of three phase grid voltages.



5. Enhanced Control of RSC and GSC for LVRT and HVRT

As illustrated in section III, the difficulty of the LVRT/HVRT control of the RSC is the uncontrolled transient over current on the rotor side of the DFIG caused by the transient EMF part of the stator flux, and the over voltage and fluctuation of the DC link. This section is focused on the enhanced feedback control of the RSC and the GSC to improve the dynamic performance of rotor current and DC voltage, and enhance the LVRT/HVRT capability by reactive power control. The control scheme diagram is shown in Figures 9 and 10.

Figure 9. Enhanced control of the GSC.



5.2. Enhanced GSC Control

During the normal operation, with a reference frame oriented along with the grid voltage vector by the vector control technique, the DC voltage and reactive power from the GSC to the grid are decoupled by controlling d - and q -axis components of the line side current. In order to maintain the DC voltage, the active current i_{gd} should be controlled according to its reference value. As Equation (12) shows, i_{gd} is proportional to u_{gq} during the steady-state operation, so u_{gq} should be kept unchanged during high voltage conditions, u_{gd} should be added to:

$$v_{gd} = \sqrt{V_{glrms, \max}^2 - v_{gq}^2} \quad (27)$$

Based on Equation (12), the i_{gq} reference value should be set to:

$$i_{gq_ref} = \frac{v_{gd} - E_{gd}}{\omega_s L_g} \quad (28)$$

The load current feed forward control is used to improve the transient performance of the GSC, and reduce the fluctuation of the DC-link voltage of the PWM converter [23]. It is difficult to measure the DC-link current, therefore, the load current i_{RSC} can be obtained by (P_r/V_{dc}) , which is expressed as:

$$i_{RSC} = \frac{P_r}{V_{dc}} = \frac{3(u_{dr}i_{dr} + u_{qr}i_{qr})}{2V_{dc}} \quad (29)$$

6. Case Studies

The case studies were carried out with a 1.5-MW DFIG WTG to verify the efficacy of the proposed LVRT and HVRT protection schemes, and the enhanced control methods using Matlab/Simulink Simpower Systems. The parameters of the WTG are listed in the Appendix. The threshold of the rotor current to activate the crowbar protection is 2 p.u., and the threshold of the DC voltage to activate the chopper protection is 1.15 p.u., and 1.05 p.u. to deactivate. Three case studies were conducted and compared. The case study scenarios are listed in Table 1. For the case studies, a three-phase fault is applied at 2 s with 200 ms duration causing the grid voltage drop of 0.2 p.u. and the voltage swell of 1.3 p.u. during the grid fault recovery (starting at 2.2 s).

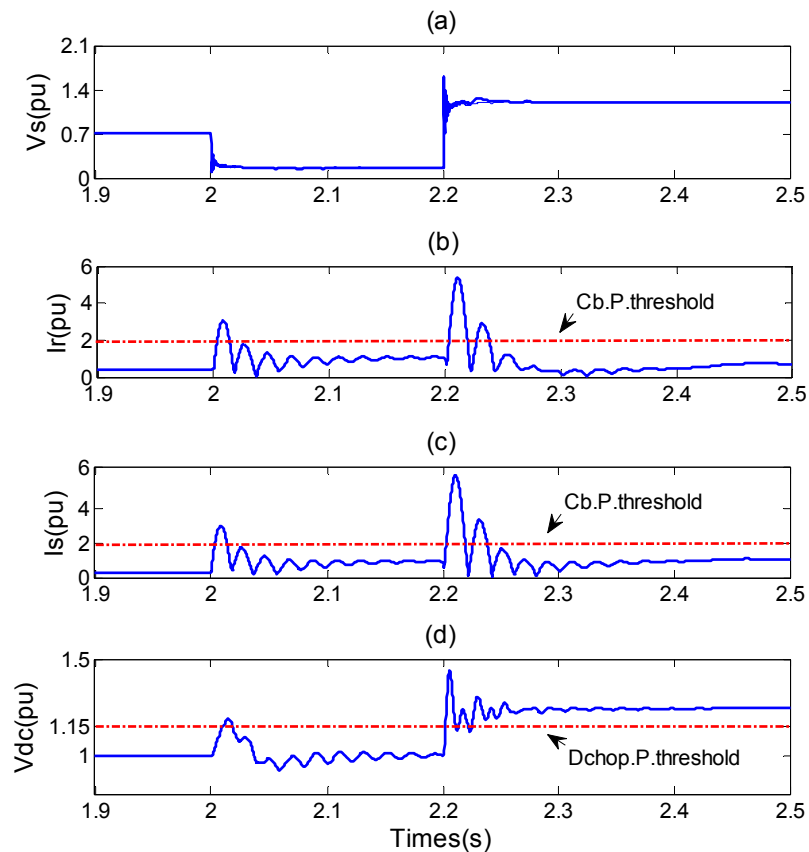
Table 1. Control schemes for case studies.

Case	LVRT and HVRT Protection	LVRT and HVRT Control
1	Crowbar disabled	RSC without enhanced control
	DC_Chopper disabled	GSC without enhanced control
2	Crowbar disabled	RSC with enhanced control
	DC_Chopper disabled	GSC with enhanced control
3	Crowbar enabled	RSC with enhanced control
	DC_Chopper enabled	GSC with enhanced control

Figure 11 shows the simulation results of Case 1 which does not have the crowbar or the DC chopper protection scheme or the enhanced converter control schemes. Figure 11a shows the voltage

profile of the pre-fault, fault and recovery periods. It is shown in Figure 11 b,c and d that the rotor current, stator current and the DC link voltage exceeded the protection settings and the WTG would have tripped due to this.

Figure 11. The responses of the DFIG WTG for case 1: (a) stator voltage; (b) rotor current; (c) stator current; and (d) DC voltage.



The simulation results of Case 2 are shown in Figure 12. The comparison results of rotor and stator currents with and without the enhanced RSC and GSC control are shown in Figure 12a,b. It is shown that the current overshooting has been reduced by the enhanced RSC and GSC control. The DC link voltage with and without the enhanced RSC and GSC control is shown in Figure 12c and it is shown that the DC link voltage can decrease below the threshold after about 180 ms from the starting point of the recovery. However, the DC link over-voltage is still an issue.

The DC link voltage and the DC chopper pulse of case 3 are shown in Figure 13. It is shown that the DC link voltage can be kept below the threshold by the combination of the crowbar scheme, and the enhanced RSC and GSC control and the DFIG WTG can successfully ride through the low voltage conditions and the high voltage conditions.

Figure 12. The responses of the DFIG WTG for case 2. (a) rotor current; (b) stator current; (c) DC voltage.

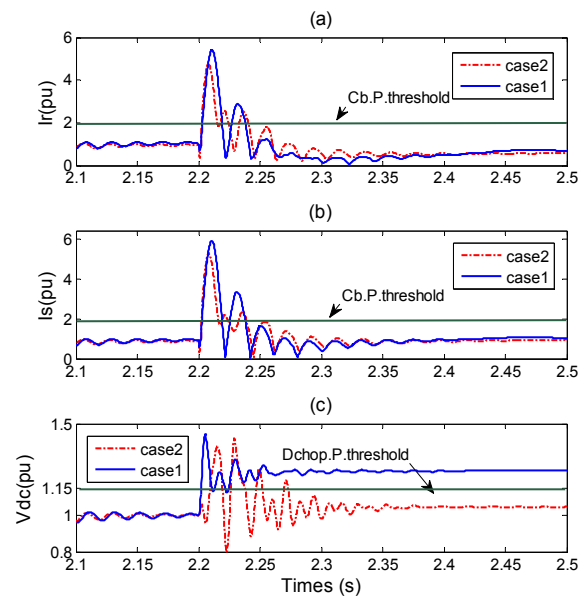
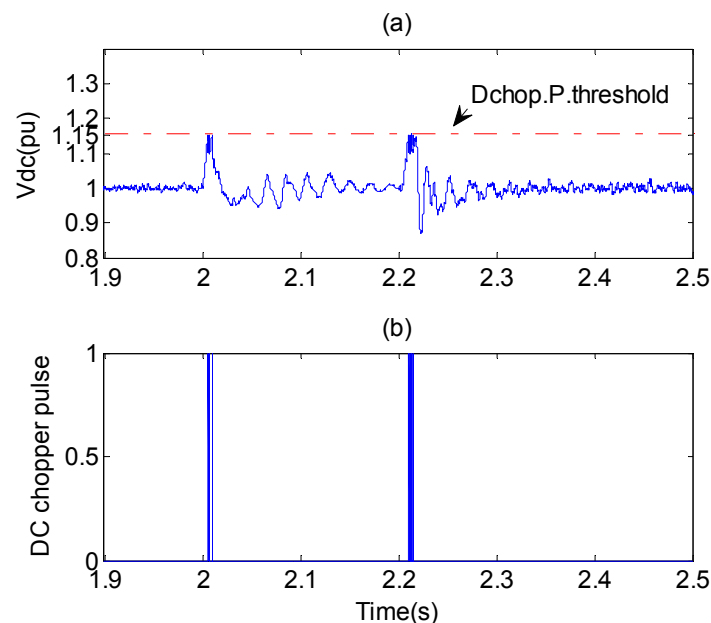


Figure 13. The DC link voltage of case 3: (a) DC voltage; (b) DC chopper circuit control pulses.



7. Conclusions

This paper analyses the transient response of the DFIG WTG with consideration of the phase-angle jump under both grid voltage sag and swell conditions. The rotor crowbar protection scheme coordinated with DC chopper protection scheme is developed to deal with both voltage sag and voltage swell. With the voltage detection method considering the phase-angle jump, the fast coordinated control of the RSC and GSC has been developed. Simulation results show that, by adding the transient stator flux compensation feed-forward factors for the vector control, the transient performance of the RSC controller can be improved. By the reactive current control of the GSC, the DC link voltage can be maintained below the threshold. With the developed crowbar protection scheme, DC chopper

protection scheme, and the enhanced RSC and GSC control, the DFIG WTG can successfully ride through the continuous low voltage and high voltage conditions.

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Conflicts of Interest

The authors declare no conflict of interest.

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