

Article

Protection Principle for a DC Distribution System with a Resistive Superconductive Fault Current Limiter

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Abstract: A DC distribution system, which is suitable for access to distributed power generation and DC loads, is one of the development directions in power systems. Furthermore, it could greatly improve the energy efficiency and reduce the loss of power transportation. The huge short circuit current is always a great threat to the safety of the components, especially the capacitors and diodes. A resistive superconductive fault current limiter (SFCL), which could respond quickly once a fault happens and limit the fault current to a relatively low level, becomes a good solution to this problem. In this paper, the operational principle of the resistive SFCL is introduced first, and then, the DC short-circuit fault characteristic of the DC distribution system with the SFCL is analyzed and the effectiveness of the SFCL verified. In order to realize the selectivity of the protection in the DC distribution system with SFCL, a new transient current protection principle based on I_p (the peak value of the current) and t_p (the transient time that the current takes to reach its peak value) is proposed. Finally, a model of a 10-kV DC distribution system with an SFCL is established and simulated in PSCAD/METDC. Simulation results have demonstrated the validity of the analysis and protection principle.

Keywords: DC distribution system; relay protection; resistive superconductive fault current limiter; transient current

1. Introduction

Nowadays, more and more electric appliances, such as electric vehicles, LED lamps, mobile phones and computers, are becoming DC consumers with the development of power electronics technology. Scholars are now putting forward the DC distribution system based on the widely studied and applied distributed generation, which supplies DC power. Compared with the AC distribution system, a DC distribution system based on a voltage source converter (VSC) presents much more advantages, such as better power quality, larger power transportation capacity, higher reliability, being more economical, having lower energy waste, and so on [1–5]. Therefore, the research on DC the distribution system has drawn more and more attention.

However, in the DC distribution system based on a VSC, the Insulated Gate Bipolar Transistor (IGBTs) will be blocked from self-protection during the DC short-circuit fault. In this case, the VSC turns into an uncontrolled rectifier, as the freewheeling diodes will feed the fault [6]. In the meantime, the DC-link capacitance discharges, and the DC current rises rapidly to a relatively large value, which may be dozens of times the normal one and poses a considerable threat with respect to the safety issue. As a result, a current-limiting device is necessary, and the resistive superconductive fault current limiter (SFCL) is a good choice because of its fast response and low power loss characteristics [7].

Currently, SFCLs are mainly applied to the AC system. For example, a 220-kV saturated iron-core superconductive fault current limiter (SISFCL) has been installed in a high voltage transmission system in Tianjin, China. There are few studies about how resistive SFCL is to be used in the DC system, and some have evaluated its performance based on the effect of limiting the voltage and current only [8,9], while some studied the suitable location for the SFCL in the DC system considering the influence of the current only [7,9,10]. However, the influence of the SFCL on the fault characteristic and relay protection of the DC distribution system has had no research results till now. At present, most of the research about relay protection of the DC distribution system does not take the current limiters into consideration [11–18]. These protection schemes all apply a great short-circuit current and a low voltage. The impact of the SFCL on the protection of the DC distribution system needs to be further investigated, for introducing the SFCL will bring about problems with respect to protection.

In this paper, the fault characteristic of the DC distribution system with the SFCL and the influence of the SFCL on protection of the DC distribution system are studied. Then, a new transient current protection principle based on I_p (the peak value of the current) and t_p (the transient time that the current takes to reach its peak value) is introduced and investigated in detail. Finally, many fault simulations are completed to verify the accuracy of the protection principle.

2. Resistive Superconducting Fault Current Limiter

A resistive SFCL is composed of a superconducting cable and a shunt resistance. The equivalent model of the SFCL is a variable resistance R_{SFCL} and a shunt resistance in parallel, which is shown in Figure 1. The shunt resistance is necessary for reducing the overvoltage. Then, the resistive SFCL uses the transformation between the superconducting state and the normal resistive state to limit the increase of the current. When a fault happens, the resistive SFCL responds rapidly, so that the fault current is limited [8].

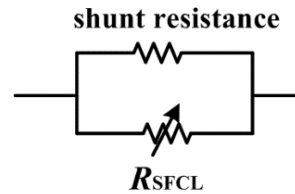


Figure 1. Structure of the resistive superconductive fault current limiter (SFCL).

In this paper, we choose Bi2212 as the material of the SFCL. The transformation of the resistive SFCL can be divided into three states as follows [8,19,20]:

(1) Superconducting state:

In this state, the current density going through the SFCL is below the critical value, and the resistance of the SFCL is very small. The electric field is:

$$E(J, T) = E_c \left(\frac{J}{J_c(T)} \right)^\alpha \quad (1)$$

where $E_c = 1 \text{ } \mu\text{V/cm}$; $5 \leq \alpha \leq 15$; J is the current density and $J_c(T)$ is the critical current density, which is dependent on the temperature of the material. Additionally, the $J_c(T)$ is:

$$J_c(T) = J_c(77) \left(\frac{T_c - T}{T_c - T_0} \right) \quad (2)$$

where T_c is the critical temperature expressed in units of K; and T_0 is the initial temperature. Here, $T_c = 95 \text{ K}$; $T_0 = 77 \text{ K}$; and $J_c(77) = 1.5 \times 10^7 \text{ A/m}^2$.

(2) Flux flow state:

When the current density exceeds the critical value, the SFCL enters this state, and the electric field starts increasing. Then, the resistance of the SFCL begins increasing, and as a result, the current starts to be limited and the temperature of the material begins to increase. Additionally, the increase of temperature makes the $J_c(T)$ decrease, so the electric field will increase continuously. In this state, the electric field is:

$$E(J, T) = E_0 \left(\frac{E_c}{E_0} \right)^\beta \frac{J_c(77)}{J_c(T)} \left(\frac{J}{J_c(77)} \right)^\beta \quad (3)$$

where $E_0 = 0.1 \text{ V/m}$ is the electric field during the transition from the superconducting state to the flux flow state. $2 \leq \beta \leq 4$.

(3) Normal resistive state:

After the temperature gets higher than the critical temperature, the SFCL enters the normal resistive state. In this state, the resistance and the electric field are mainly dependent on the current density and the temperature. Additionally, the electric field is:

$$E(J, T) = \rho(T_c) J \frac{T}{T_c} \quad (4)$$

where $\rho(T_c)$ is the normal conducting resistivity, and here, $\rho(T_c) = 1 \times 10^{-6} \text{ } \Omega \cdot \text{m}$.

Overall, the resistance of the material is:

$$R_{sc} = \frac{E}{i_{sc}} l_{sc} \quad (5)$$

where the i_{sc} is the current going through the superconducting wire and l_{sc} is the length of the wire.

Furthermore, in all three states, the heat diffusion equation is:

$$\rho_v l_{sc} SC \frac{dT}{dt} = i_{sc}^2 R_{sc} - \frac{T - T_0}{\theta_{sc}} \quad (6)$$

where $\theta_{sc} = 1/[\kappa(l_{sc} \cdot 2\pi r + 2S)]$; $\kappa = 1.5 \times 10^3$ W/(K·m²); r is the radius of the superconducting wire and $C = 1.58T$ (in units of J/(kg·K)). Here, the length of the superconducting wire is $l_{sc} = 200$ m, and the volumetric density is $\rho_v = 6$ g/cm³. When the critical current is chosen, the cross-sectional area can be calculated by $S = I_c/J_c$.

The resistive SFCL model is established in PSCAD/EMTDC. In order to limit the surge current discharged by the capacitor, the resistance of the SFCL needs to become large enough quickly before the current gets to the peak value. In this paper, the length of the superconducting wire is assumed to be 200 m. In the DC distribution system, when a fault happens at 0.5 s, the variation of the resistance of the SFCL with respect to time is as shown in Figure 2.

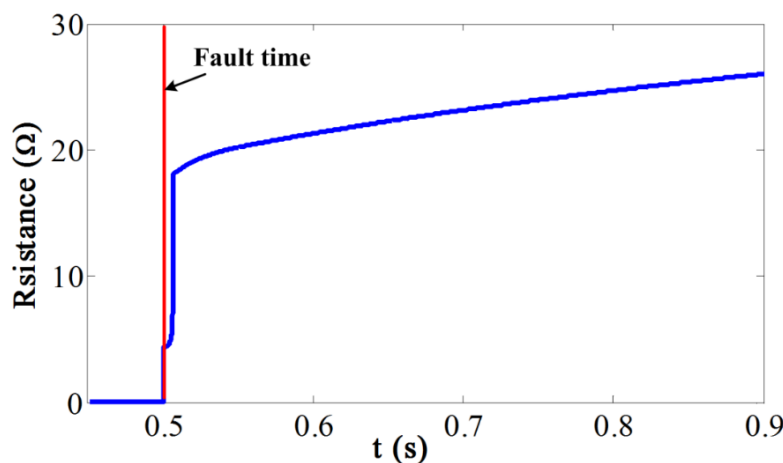


Figure 2. Variation of the resistance of the SFCL.

3. Analysis of Fault Characteristic in the DC Distribution System with the SFCL

When a DC short-circuit fault happens on the DC side of VSC, the IGBTs are blocked from self-protection. All of the diodes are blocked because of the reverse voltage. As a result, the AC system is separated from the DC side, and the AC current drops to zero [6]. If a SFCL is not used, the fault characteristic can be divided into four stages [16], which are shown in Figure 3. In the first stage, the capacitor discharges rapidly, and the DC current gets to a large value. This is a great threat to the safety of the capacitors. Additionally, in the third stage, the AC system is equivalent to a three-phase short circuit. These are unfavorable conditions for the system, and the large AC current is harmful for the diodes. Whether the third stage appears is decided by the damping property of the circuit. However, the installation of a resistive SFCL can limit the discharge of the capacitor and make the third stage disappear. Therefore, the resistive SFCL can limit the fault current on both the AC and DC sides, and

the system will be safer. Then, the breaking capacity of the DC circuit breakers could be reduced. The fault characteristic under a particular situation of the simulation system is shown in Figures 4 and 5.

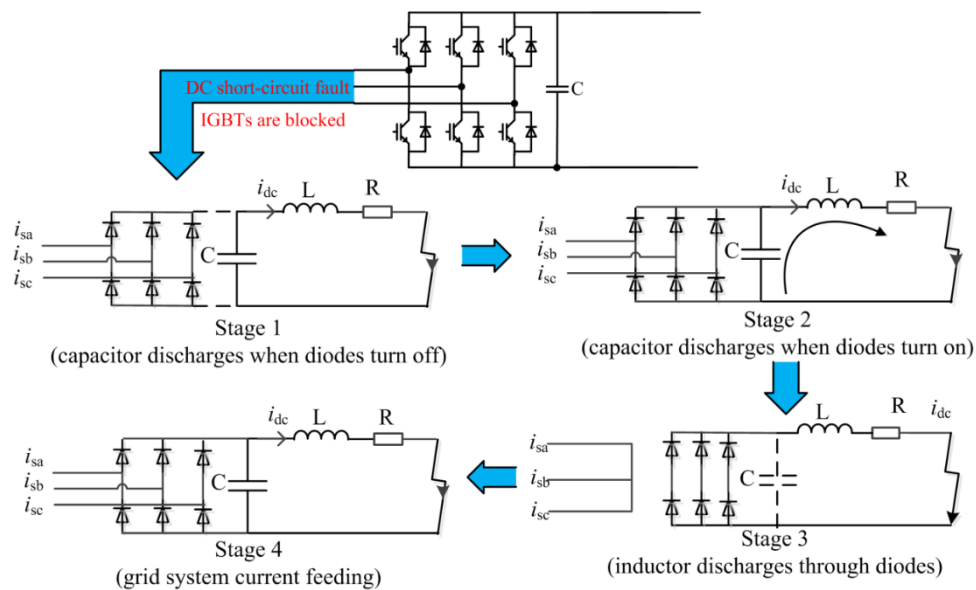


Figure 3. The four stages for the rectifier during a DC short-circuit fault without the SFCL.

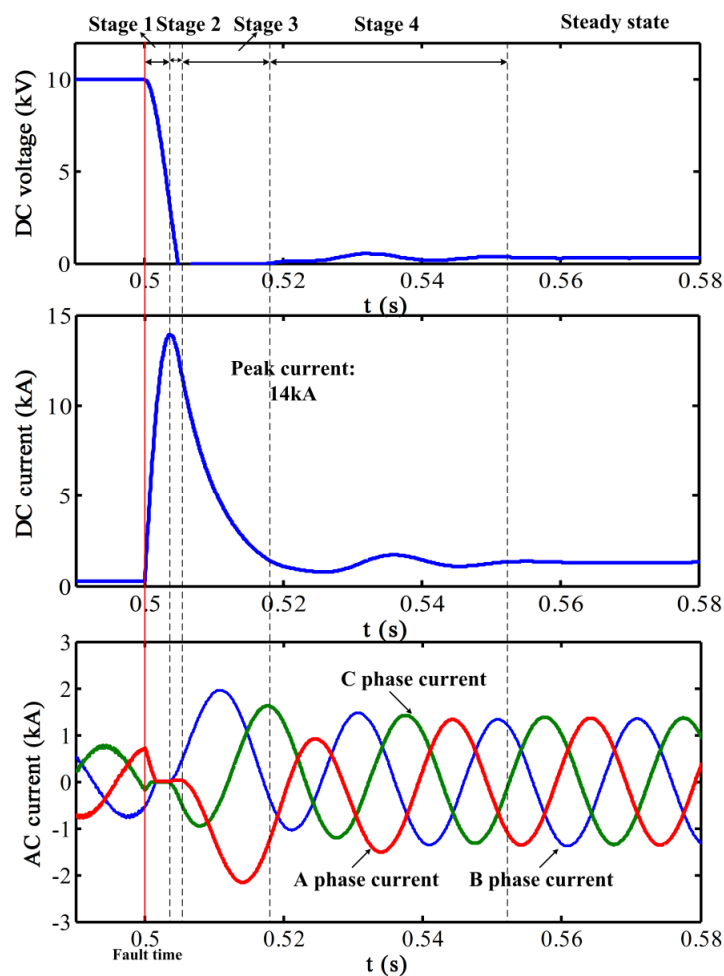


Figure 4. The fault characteristic of the voltage source converter (VSC) without the SFCL during a DC short-circuit fault.

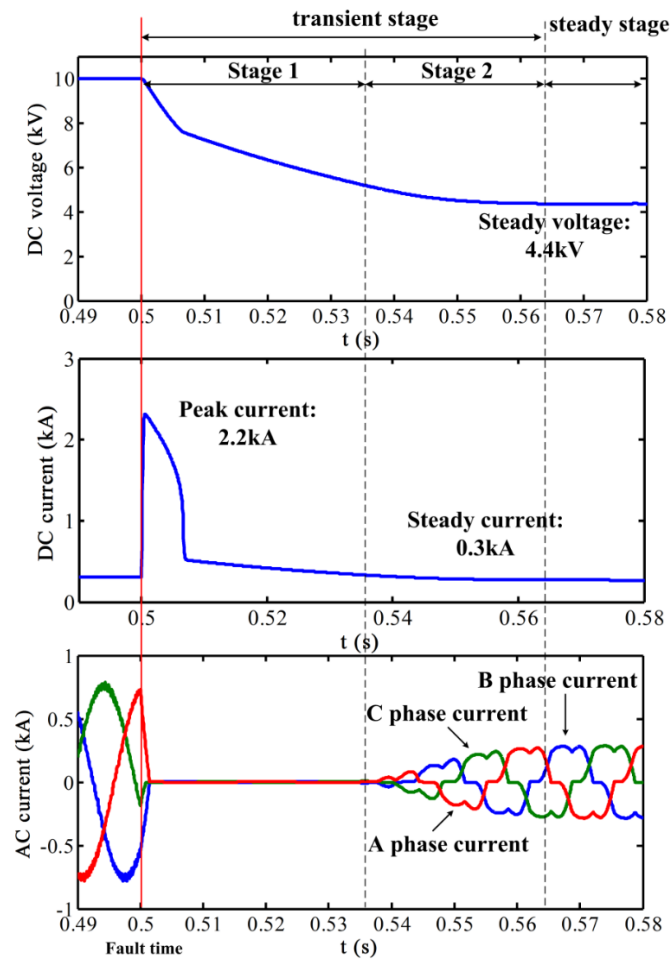


Figure 5. The fault characteristic of the VSC with the SFCL during a DC short-circuit fault.

As shown in Figures 4 and 5, the peak current is reduced from 14 down to 2.20 kA after the SFCL is installed, about an 84.3% decrease. The peak current is limited because of the fast operation of the SFCL. Furthermore, due to the SFCL, the time that the current takes from the beginning of the fault to the peak becomes shorter. In the steady state of the fault, the current is limited to a very low level with the increasing resistance of the SFCL. At this time, the current is close to 0.3 kA, even smaller than the normal value.

In addition, the decrease of the DC voltage is also limited by the SFCL. The rate of descent is reduced greatly. Additionally, the steady value of the voltage rises from about 1 up to 4.4 kV, about a 77% increase. Furthermore, once the SFCL is installed, the voltage will remain at a relatively high value, and the third stage disappears. Therefore, the resistive SFCL overcomes the equivalent three-phase short circuit on the AC side. For different distances of faults, the voltage of the steady state changes little.

The analysis of the fault characteristic is carried on in detail as follows.

3.1. The Transient State of the DC Short-Circuit Fault with the Resistive SFCL

Stage 1: In the initial period of the fault, when the resistive SFCL is included and the IGBTs are blocked, the capacity, the cable and the SFCL compose the discharge circuit on the DC side. During this period, the voltage begins to decrease, and the DC current will rise to the maximum, then decrease. The equivalent circuit is shown in Figure 6.

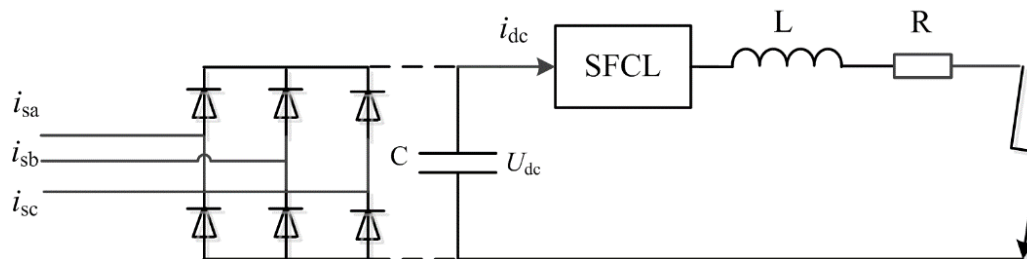


Figure 6. The equivalent circuit of the VSC in the initial phase of the fault.

According to the circuit, the voltage and current of the DC side can be calculated by:

$$\begin{cases} u_{dc} = (R + R_{SFCL})i_{dc} + L \frac{di_{dc}}{dt} \\ i_{dc} = -C \frac{du_{dc}}{dt} \end{cases} \quad (7)$$

where i_{dc} is the current of the circuit; u_{dc} is the voltage of the capacitor; C is the capacity; R and L are the equivalent parameters of the cable from the exit of the VSC to the fault position; R_{SFCL} is the resistance of the SFCL, which changes to nonlinear. Additionally, it is easy to know that the rate of descent of the voltage and peak current reduces greatly. The fault waveforms of Stage 1 are shown in Figure 5.

Stage 2: With the decrease of the DC voltage, the diodes start to turn on after the DC voltage is less than the AC voltage. The AC current begins to rise from zero. Then, the fault reaches the steady state gradually. The equivalent circuit at this stage is the same as the circuit in the steady state, which is shown in Figure 7. However, the discharge of the capacitor plays a major role at this stage. Additionally, the fault waveform of Stage 2 is shown in Figure 5.

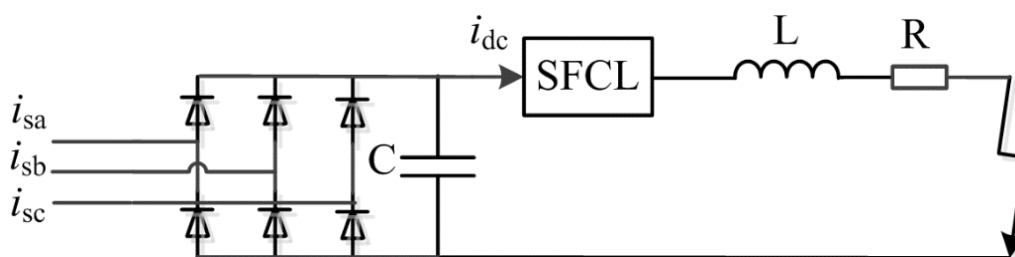


Figure 7. The uncontrolled rectifier.

3.2. The Steady State of the DC Short-Circuit Fault with the Resistive SFCL

In the steady state, the VSC changes into an uncontrolled rectifier, as shown in Figure 7. The output voltage is impacted by the flow angle of the diodes. Additionally, the voltage changes between 2.34 U and 2.45 U , where U is the RMS value of the phase voltage of the AC system.

Because the resistance of the SFCL is much greater than the impedance of the cable, the analysis can be done ignoring the inductance. Then, the flow angle (θ) of the diodes is influenced by ωRC , and the relationship is shown in Figure 8.

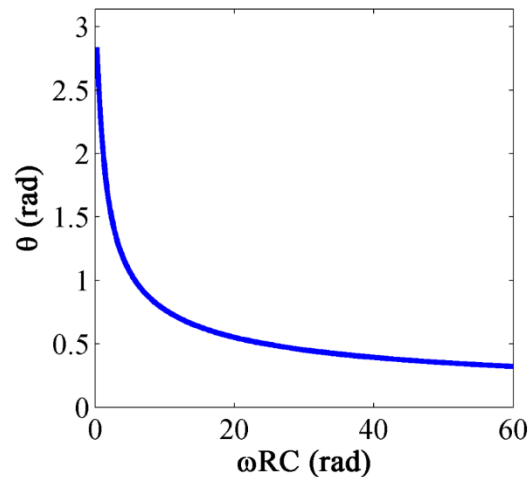


Figure 8. The flow angle (θ) versus ωRC .

From Figure 8, it is known that the flow angle (θ) will decrease, with the increase of ωRC . The critical condition is $\omega RC = \sqrt{3}$. When $\omega RC > \sqrt{3}$, the AC current of the uncontrolled bridge rectifier will be discontinuous, as shown in Figure 5. With the large increase of the resistance in the fault circuit, the current in the steady state may be close to or less than the value in the normal condition. Additionally, in the steady state of the fault, the voltage increases greatly compared with the condition that the SFCL is not installed. On the other hand, the current in the steady state of the fault cannot be used for protection; otherwise, the protection will be incorrectly tripped.

4. Protection Principle and Verification

4.1. Transient Current Protection and Its Coordination

Overcurrent protection using the peak current is widely used in the DC distribution system. As we know, before the integration of the SFCL, the overcurrent protection using the peak current could coordinate very well [16]. It can satisfy the requests for protection. However, after the integration of the SFCL, the difference among the peak currents of different fault positions becomes little. In this situation, if we still apply the overcurrent protection, the reliability coefficient will be too small to satisfy the request for sensitivity and reliability. Therefore, the overcurrent protection needs to be improved.

In this paper, it is assumed that the fault resistance of the DC short-circuit fault between the cables is very small. According to the analysis above, we can get the relationship between the fault position and peak current, as shown in Figure 9. If the SFCL is included, the peak current has an approximately linear relationship with the position of the fault. However, the peak current changes a little with respect to the increasing of the distance of the fault. However, the time that the current takes to reach the maximum varies greatly when a fault happens at different positions, as shown in Figure 10. Then, if the ratio of the peak current to the time is used for the protection, as shown in Figure 11, the sensitivity and reliability could be ensured.

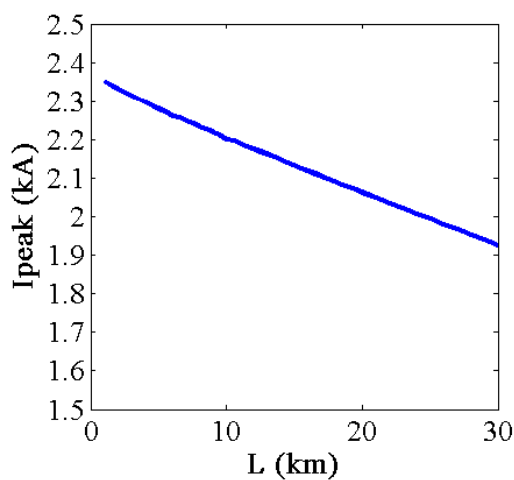


Figure 9. Peak currents of different fault positions.

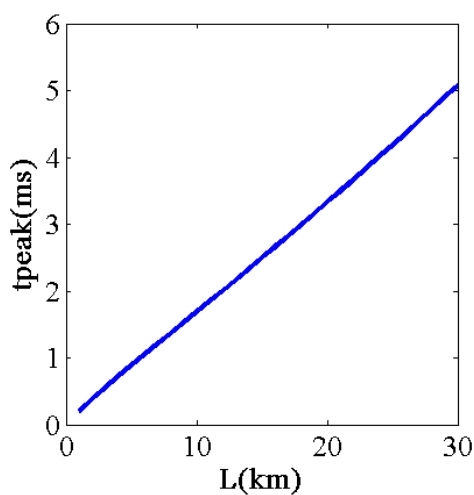


Figure 10. The time that the current takes to reach the peak *versus* the fault position.

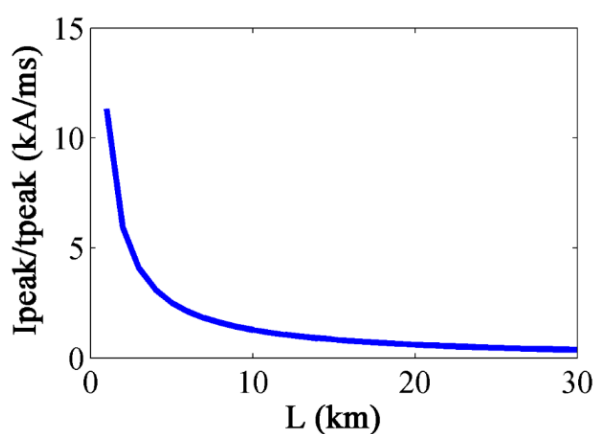


Figure 11. The ratio of the peak current to the time *versus* the fault position.

4.1.1. Transient Current Protection

Under the condition that the SFCL is installed, transient current protection is set according to the principle that the threshold is above the ratio of $I_{k \text{ peak end } 1}$ (the peak of the short circuit current at the end

of the line) to $\Delta t_{k \text{ peak end } 1}$ (the time that the current takes to reach the maximum from the beginning of the fault). The operating criterion is:

$$\frac{I_{\text{peak}}}{\Delta t} > \left(\frac{I_{\text{peak}}}{\Delta t} \right)_{\text{set-1}}^I \quad (8)$$

The threshold is:

$$\left(\frac{I_{\text{peak}}}{\Delta t} \right)_{\text{set-1}}^I = K_{\text{rel}}^I \times \frac{I_{k \cdot \text{peak} \cdot \text{end} \cdot 1}}{\Delta t_{k \cdot \text{peak} \cdot \text{end} \cdot 1}} \quad (9)$$

where I_{peak} is the maximum of the short circuit current during the fault; Δt is the time that the current takes to reach the maximum from the start of the fault and K_{rel}^I is the reliability coefficient, which can be 1.2–1.3. Therefore, transient current protection can only protect about 80% or less of the line. However, the protection will operate without delay once the ratio exceeds the threshold.

4.1.2. Time-Limit under Voltage Protection Started by the Transient Current

Time-limit overcurrent protection coordinates with the downstream line by the time delay. The time delay can be 0.1 s or more in the DC system [16], considering the coordination with the downstream line and the operation time that the DC circuit breaker takes. However, when the SFCL is installed, the current has already been in the steady state at 0.1 s, and the value is close to the normal value after the fault. Therefore, traditional time-limited overcurrent protection cannot be applied directly to the DC system with the SFCL.

However, the DC voltage will decrease to a relatively low level at 0.1 s after the fault. When the resistive SFCL is installed, the voltage in the steady state changes little with the change of the fault position. Therefore, the under voltage protection alone cannot ensure the selectivity. Then, the transient current protection is combined with the under voltage protection, called the time-limit under the voltage protection started by the transient current. This kind of protection still protects the whole line and does not exceed the range of the transient current protection of the downstream line. It coordinates with the downstream line by a time delay. The starting value is the threshold of the transient current protection, and it can guarantee the selectivity of protection. After the time delay, whether the breaker operates is decided by the under voltage protection. The operating criterion is:

$$\left(\frac{I_{\text{peak}}}{\Delta t} > \left(\frac{I_{\text{peak}}}{\Delta t} \right)_{\text{set-1}}^I \right) \cap (t = t_{\text{set-1}}^I) \cap (U_{\text{dc}} < U_{\text{set-1}}^{\text{II}}) \quad (10)$$

The threshold is:

$$\left(\frac{I_{\text{peak}}}{\Delta t} \right)_{\text{set-1}}^{\text{II}} = K_{\text{rel}}^{\text{II}} \times \left(\frac{I_{\text{peak}}}{\Delta t} \right)_{\text{set-2}}^I = K_{\text{rel}}^{\text{II}} \times K_{\text{rel}}^I \times \left(\frac{I_{k \cdot \text{peak} \cdot \text{end} \cdot 2}}{\Delta t_{k \cdot \text{peak} \cdot \text{end} \cdot 2}} \right) \quad (11)$$

$$t_{\text{set-1}}^{\text{II}} = t_2^I + \Delta t \quad (12)$$

$$U_{\text{set-1}}^{\text{II}} = K_{\text{u-rel}}^{\text{II}} \times U_{\text{N}} \quad (13)$$

where $(I_{\text{peak}}/\Delta t)_{\text{set-1}}^{\text{II}}$ is the starting value of the under voltage protection; $(I_{\text{peak}}/\Delta t)_{\text{set-2}}^{\text{I}}$ is the threshold of the transient current protection of the downstream line; $K_{\text{rel}}^{\text{II}}$ is the reliability coefficient, which can be 1.1–1.2; $U_{\text{set-1}}^{\text{II}}$ is the threshold of the under voltage protection; U_N is the rated voltage; $K_{\text{u-rel}}^{\text{II}}$ is the reliability coefficient; $K_{\text{u-rel}}^{\text{II}} = 0.5 - 0.6$, t_2^{I} is the inherent opening time of the transient current protection and Δt is the time delay, always $\Delta t = 0.1 - 0.3$ s.

4.2. Case Study

A 10-kV DC distribution system with the resistive SFCL is simulated by PSCAD/EMTDC. The structure of the system is shown in Figure 12. It is assumed that DC short-circuit fault occurs at 0.5 s in the different positions of the DC side. The fault resistance is 0.001 Ω . Each of the lines in the system is 10 km long. The equivalent parameter of the line is $R = 0.078$ Ω/km and $L = 0.48$ mH/km. When the system operates in the normal condition, the currents that go through each line are $I_{\text{line1}} = 0.35$ kA, $I_{\text{line2}} = 0.252$ kA and $I_{\text{line3}} = 0.157$ kA.

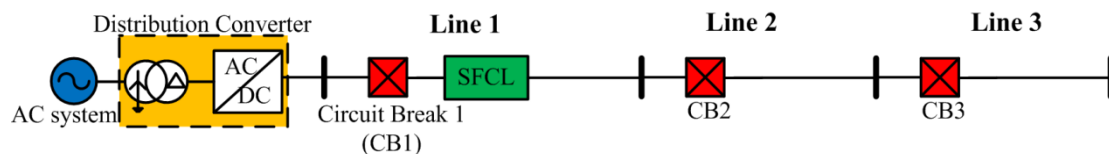


Figure 12. The structure of the DC distribution, including the SFCL.

According to the analysis above, the thresholds of protection are listed in Table 1. Because Line 3 is the last section of the transmission line, traditional overcurrent protection can be applied for Line 3. Therefore, the threshold can be several times the load current, and the instantaneous overcurrent protection protects the whole line. In this situation, the time-limited overcurrent protection is the backup protection of Line 3. Then, the principle of the protection mentioned above is verified in this system, and the result is listed in Table 2.

Table 1. The thresholds of protection.

Line		Parameters	Thresholds
Line1 (CB1)	I	transient current threshold	1.424 (kA/ms)
	II	transient current threshold	0.812 (kA/ms)
		under voltage threshold	5.0 (kV)
		time delay threshold	0.2 (s)
Line2 (CB2)	I	transient current threshold	0.738 (kA/ms)
	II	transient current threshold	0.496 (kA/ms)
		under voltage threshold	5.0 (kV)
		time delay threshold	0.2 (s)
Line3 (CB3)	I	instantaneous overcurrent threshold	1.5 (kA)
	II	time-limit overcurrent threshold	1.5 (kA)
		time delay threshold	0.2 (s)

Table 2. The action situation of each CB.

Line	Fault Location (Percentage of the Line)	The Action of Breakers (Time for Receiving Trip Instruction) (s)		
		CB1	CB2	CB3
Line1	30%	0.5010	#	#
	80%	0.5017	#	#
	100%	0.7021	#	#
Line2	30%	#	0.5027	#
	80%	#	0.5034	#
	100%	#	0.7038	#
Line3	30%	#	#	0.5026
	80%	#	#	0.5033
	100%	#	#	0.5039

Note: # is defined as that the circuit breaker (CB) of the Line will not operate.

From the result, it can be seen that the presented protection principle could clear the fault quickly and accurately. The transient current protection can protect about 80% of the length of the section. Additionally, it operates without a time delay. With the increasing of the distance of the fault, the rising of current slows down. Therefore, the operation time of the transient current protection becomes longer, but still operates very quickly. On the other hand, the time-limited under voltage protection started by the transient current can protect the whole length of the line with a time delay. It can operate at about 0.2 s or more after the fault. This kind of protection may operate slowly, but it can guarantee the selectivity between the upstream line and the downstream line.

Therefore, for the DC distribution system, including the SFCL, this principle of protection can operate within a short time for the feeder faults, and the coordination of the upstream and downstream protective relays is ensured.

5. Conclusions

A resistive SFCL has good performance for current limiting in the DC distribution system. It is suitable for a system with a high fault current and a high current rising speed. However, the integration of the SFCL has a serious influence on the coordination of the protection. In this paper, the influence of the SFCL on the protection and the fault characteristic is analyzed in detail. Then, the transient current protection is presented, and the time-limited under voltage protection started by the transient current is proposed. Finally, the principle of the protection is verified in PSCAD/EMTDC. The result demonstrates that this principle of protection can ensure the coordination of the upstream and downstream protective relays. Nevertheless, a DC short-circuit fault with fault resistance is not considered in this paper, which would be the main work in the future.

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Author Contributions

Shimin Xue researched the protection principle and drafted the article. Feng Gao provided the results of the simulation. Valuable comments on the first draft were received from Feng Gao, Wenpeng Sun and Botong Li. All four were involved in revising the paper.

Conflicts of Interest

The authors declare no conflict of interests.

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