

Investigating the ability of shunt hybrid power filter based on SRF method under non-ideal supply voltage

Mohamed A Omran^{1,2}, Izzeldin I Mohd¹, Mohamad M Almelian^{1,3}, Usman Ullah Sheikh⁴ and Mustafa E A A Bofares⁵

¹Faculty of Electrical and Electronics Engineering, University Malaysia Pahang (UMP), 26600 Pekan, Pahang, Malaysia

²College Of Technical Sciences – Bani-Walid, Libya

³Higher Institute Of Sciences And Technology, Mezdah, Libya

⁴Faculty of Electrical Engineering, Universiti Teknologi Malaysia, 83100 UTM Skudai, Johor Bahru, Johor, Malaysia

⁵Higher and Intermediate Institute of Engineering Professions-Bani-Walid, Libya

E-mail: omranmohamed346@yahoo.com

Abstract. This study presents the capacity of a self-tuning filter based on the synchronous reference frame method with a fuzzy logic controller for the improvement of the efficiency of harmonic suppression of a shunt hybrid active power filter in an unbalanced distorted and un-distorted voltage supply conditions. The simulation results indicated that the filter with a fuzzy logic controller had a good filtering performance in steady and transient states, irrespective of whether the voltage supply is distorted or unbalanced.

1. Introduction

The issues with power quality have become critical in recent times due to the use of electronic circuits with non-linear behaviors in industrial and domestic devices such as inverters, power supply switches, single/ three phases of rectifiers. These circuits generate non-sinusoidal currents in the distribution power networks. The issues of reactive and active power occur usually due to the connection of nonlinear single-phase loads with either three-phase or four-wire structures. Even when these loads are stable, harmonic currents circulate through the neutral conductor, and this is bound to happen as a result of the presence of zero sequence elements [1-2]. These currents might have higher amplitudes compared to those of the component phase current. This difference of current amplitudes can lead to damage the transformers and neutral conductor which bear these loads [3-5].

To disband this problem of current harmonics in the power distributed network, several solutions have been proposed. These solutions can be grouped into two: the first group is the conditioning loads group which provides confer equipment with less sensitivity to energy instabilities, thereby, allowing service continuity even when there is a significant distortion in the voltage [6]. The other technique is the installation of conditioning structures which minimizes harmonic disturbances.

Harmonic currents can be minimized with passive filters (PFs) to compensate for reactive power. However, there are some issues with these PFs, such as heavy and bulky devices, parallel and series resonance with load and source impedances. The active filters were established to reduce the issues with the passive filters [4, 7, and 8] but they have high operational costs and also not appropriate for moderate or high voltage conditions. A synergistic combination of the passive and active filters in the



form of a hybrid power filter (HPF) is appealing [7]. Various forms of hybrid filters with varying control systems have been suggested for load harmonics compensation.

These structures allow the separate use of parallel/series active filters to design small-rated active filters. The harmonic extraction method for the detection of voltage and current harmonic as well as sub-harmonics and components are mainly according to either direct or indirect control technique which is dependent upon the theory of instantaneous reactive power [9]. The efficiency of the active power filters has also been improved using adaptive notch filters, synchronous rotational reference frame, flux-based controllers, and nonlinear controllers. However, most of these strategies are difficult to implement as they contain a number of transformations [10].

The fuzzy logic controllers [4,6] have recently gained attention in terms of application to APFs. A fuzzy logic controller is an ideal approach compared to the conventional methods because they do not require any accurate mathematical model, can handle non-linearity, can apply inaccurate inputs, and are more vigorous to implement [11].

In this paper, a synchronous reference frame (SRF) with a self-tuning filter (STF) was used to shape ac current and regulate dc voltage intended for a three-phase shunt HAPF under ideal and non-ideal power source conditions.

2. Control algorithms of the SHAPF

Figure 1 depicts the controlling algorithm block diagram. The signals of feedback are detected from the load currents, as well as AC source and DC bus voltages of SAPF [12]. The three-phase currents (i_L^a , i_L^b and i_L^c) were converted from (a-b-c) phases reference frame to ($\alpha\beta$) phases static reference frame currents i_α and i_β using

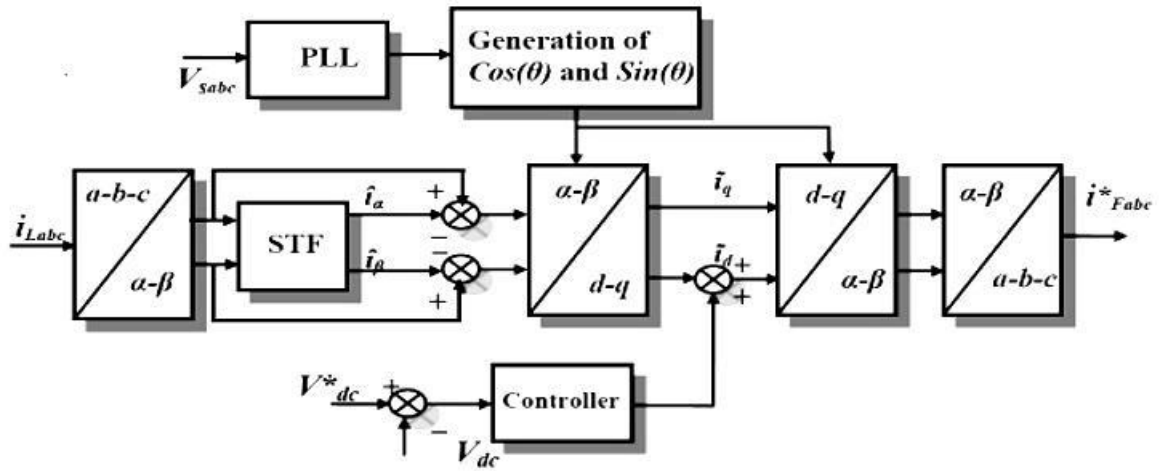


Figure 1. SRF control strategy with STF based on SHAPF.

$$\begin{bmatrix} i_\alpha \\ i_\beta \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -1/2 & -1/2 \\ 0 & \sqrt{3}/2 & -\sqrt{3}/2 \end{bmatrix} \begin{bmatrix} i_L^a \\ i_L^b \\ i_L^c \end{bmatrix} \quad (1)$$

The instantaneous current could be represented as the total fundamental and alternative components:

$$\begin{cases} i_\alpha = \tilde{i}_\alpha + \tilde{i}_\alpha \\ i_\beta = \tilde{i}_\beta + \tilde{i}_\beta \end{cases} \quad (2)$$

Then the STF directly extracts the fundamental components from the currents in the α - β axis. Then, the α - β harmonic current components are generated by deducting the signals of STF input from the resultant outputs (Figure 1). The signals from this computation are the AC elements (\tilde{i}_α and \tilde{i}_β) which are the corresponding harmonic elements generated in the stationary reference frame by the load currents for all phases i_L^a , i_L^b and i_L^c [13-14]. $\cos(\theta)$ and $\sin(\theta)$ can be generated from the phase voltage source (V_{sa} , V_{sb} and V_{sc}) with the help of Phase Locked Loop (PLL). The currents \tilde{i}_α and \tilde{i}_β within the d-q reference frame can be expressed as

$$\begin{bmatrix} \tilde{I}_d \\ \tilde{I}_q \end{bmatrix} = \begin{bmatrix} \sin(\theta) & -\cos(\theta) \\ \cos(\theta) & \sin(\theta) \end{bmatrix} = \begin{bmatrix} \tilde{I}_\alpha \\ \tilde{I}_\beta \end{bmatrix} \quad (3)$$

The reference currents (I_F^*) in the a-b-c frame are expressed as

$$\begin{bmatrix} I_{Fa}^* \\ I_{Fb}^* \\ I_{Fc}^* \end{bmatrix} = \sqrt{2}/3 \begin{bmatrix} 1 & 0 \\ -1/2 & \sqrt{3}/2 \\ -1/2 & -\sqrt{3}/2 \end{bmatrix} \begin{bmatrix} I_\alpha^* \\ I_\beta^* \end{bmatrix} \quad (4)$$

The STF is used in Figure 2 as a harmonic isolator rather than filters which based on classical extraction methods. Hong-sock Song in [5] had earlier justified the inclusion of the SRF method

$$V_{XY}(t) = e^{j\omega t} \int e^{-j\omega t} V_{XY}(t) dt \quad (5)$$

By transfer function of filter, the equation (5) can be expressed: [15]

$$\tilde{I}_\alpha(s) = \frac{k}{s} [I_\alpha(s) - \tilde{I}_\alpha(s)] - \frac{WC}{s} \tilde{I}_\beta(s) \quad (6)$$

$$\tilde{I}_\beta(s) = \frac{k}{s} [I_\beta(s) - \tilde{I}_\beta(s)] + \frac{WC}{s} \tilde{I}_\alpha(s) \quad (7)$$

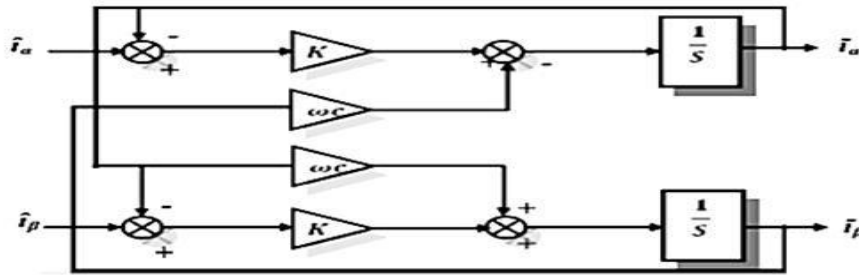


Figure 2. The structure of self-tuning filter tuned STF.

3. DC capacitor voltage control

The fuzzy control (FL) algorithm was used for the control of the DC capacitor voltage according to the processing of DC voltage error as well as its variation to maintain the dc voltage as a reference value, improve the SHAPF performance, and minimize the THD as shown in figure3. While designing a fuzzy control system [16]. FL was described by seven fuzzy sets entailing NM, NB, ZE, NS, PM, PS, and PB for its input and output variables, function of triangular membership utilized for the simplicity, defuzzification by means of the height technique and implication by means of Mamdani-type min-operator.

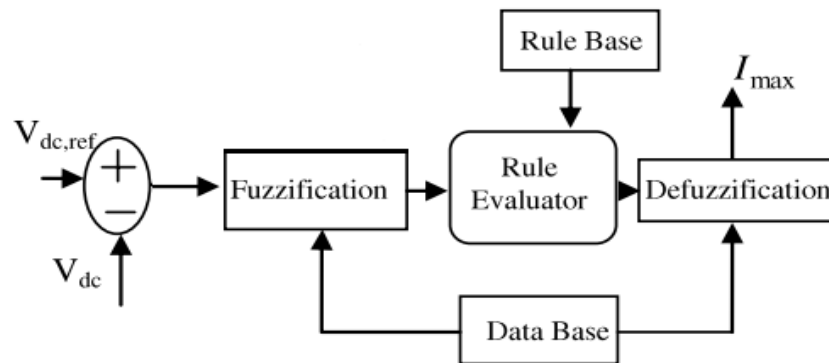


Figure 3. The structure of FLC.

4. Results and discussion

The source voltage, source current with filter, compensation current, compensation current, compensation current, dc link voltage and THD under conditions of balanced un-balanced and non-sinusoidal supply voltage with FLC are detailed in Figures 4, 5, 6 and 7. The specification and circuit parameters are used in the simulations are written in table 1 below:

Table 1. presented system parameters.

PARAMETERS	VALUE
Source voltage	240V
voltage frequency	50 HZ
Inductor Ls	0.0001 H
Resistor Rs	0.0005 Ω
Inductor LL 1	0.05 H
Resistor RL 1	80 Ω
Inductor LL 2	0.5 H
Resistor RL 2	120 Ω
Inductor Lf	0.000075 H
Resistor Rf	0.00050 Ω
DC bus voltage	450V
DC capacitor	350 μ F

The system performance was initially investigated with nonlinear (0 to 0.25s) and additional nonlinear loads (0.25 to 0.4 s) under balanced sinusoidal conditions and found to be effective at harmonics suppression (Figures 4 and 5). The THDs of the currents were reduced from 28.45% and 29.33% to 2.08% and 1.78% respectively. Meanwhile, there was an increase of 30% in load at $t = 0.25$ s in the case of changing loads. As evidenced, the filter current was relational to the changes in the load as no significant current transition was observed. The line current remained sinusoidal and attained the steady value. Besides, the DC-link capacitor was maintained with very small ripple by the FLC at the required range (450v) during load changes.

The filter capability to compensate harmonic loads under unbalanced distorted ac source (includes a 5th and 7th harmonic components (THD= 14.67%)) is shown in Figure 6. There was a decrease in the measured THD of the source current from 29.31% and 31.25% prior to compensation to 2.23% and 2.71% post-compensation for the non-linear loads as in (Figure 7). The voltage of the dc capacitors dc was within the reference range and the voltage variation was within the limit in the controller under distorted unbalanced supply.

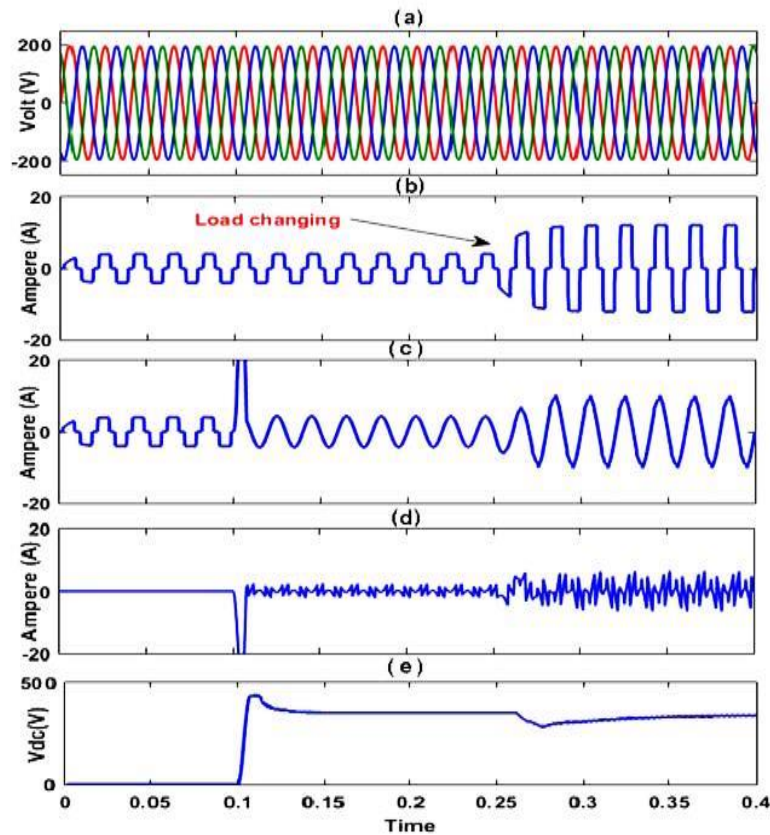


Figure 4. SHAF response under ideal voltage situation (a) Source voltage (V_s), (b) Load current (I_L), (c) load current with filter, (d) Filter compensation current (I_f), and (e) DC link voltage of system (V_{dc}).

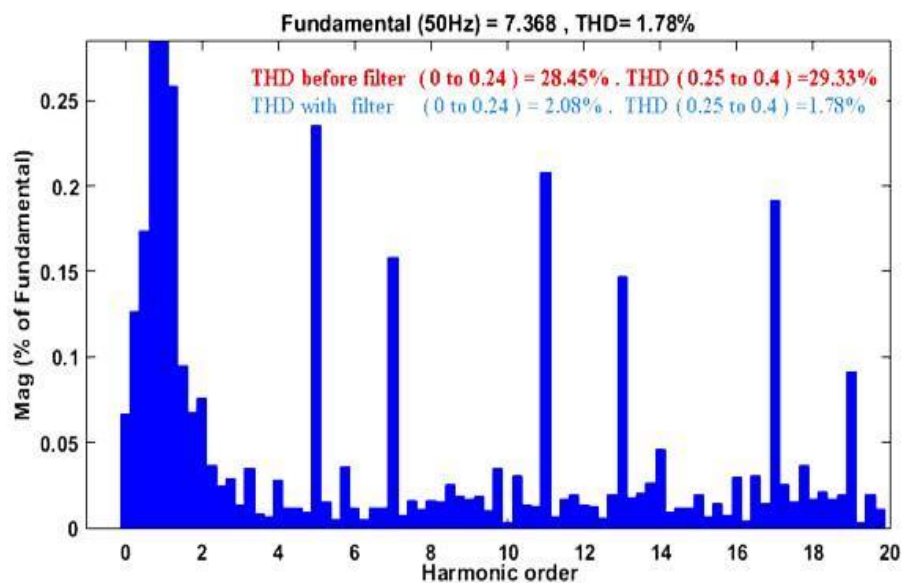


Figure 5. THD of current (I_s) under ideal voltage supply.

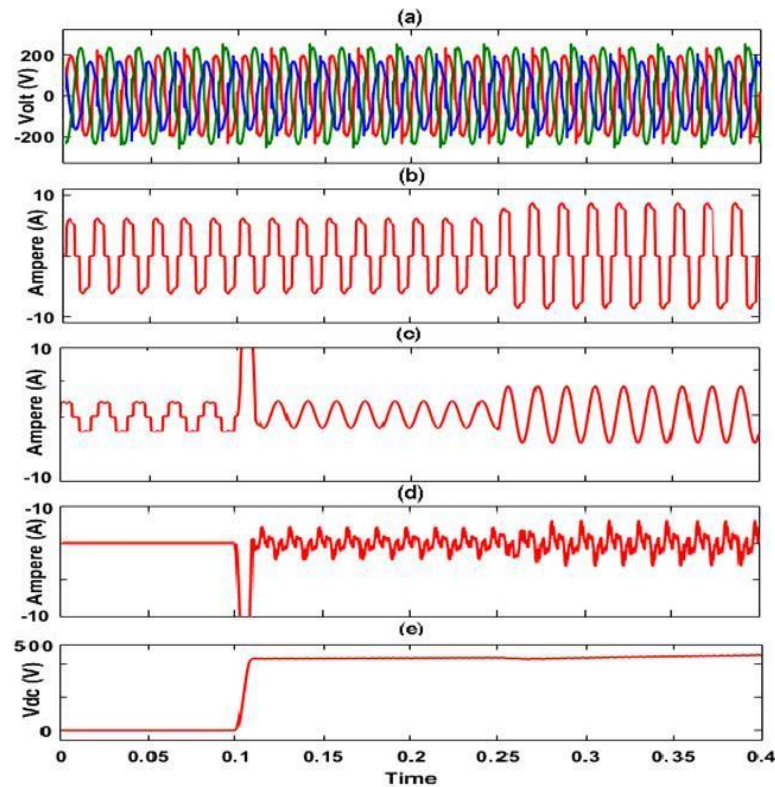


Figure 6. SHAF response under non ideal voltage situation (a) Source voltage (V_s), (b) Load current (I_l), (c) load current with filter, (d) Filter compensation current (I_f), and (e) DC link voltage of system (V_{dc}).

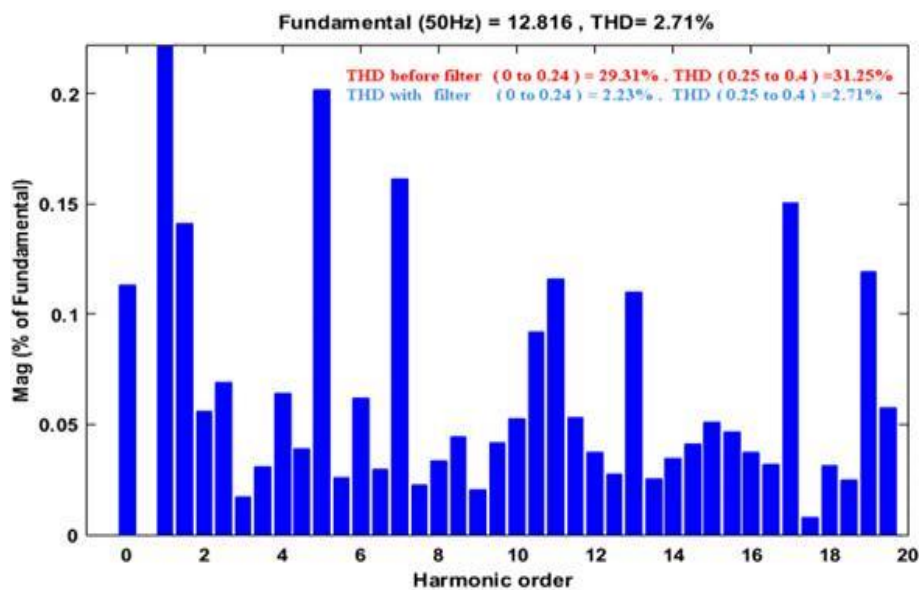


Figure 7. THD of current (I_s) under non ideal source voltage.

5. Conclusion

This paper investigated the effectiveness of a synchronous reference frame (SRF) with a self-tuning filter (STF) control strategy in controlling the performance of a three-phase SHAPF system under conditions of non-ideal and balanced supply voltage. The fuzzy logic controller was utilized for the adjustment of the DC voltage. The performance of the SHAPF system was investigated under a dynamic and steady state and under different load operating conditions. The simulation results showed the SAPF to have successfully reduced current harmonics to about 1.7 and 2.7 % for both cases of source voltages.

Acknowledgments

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